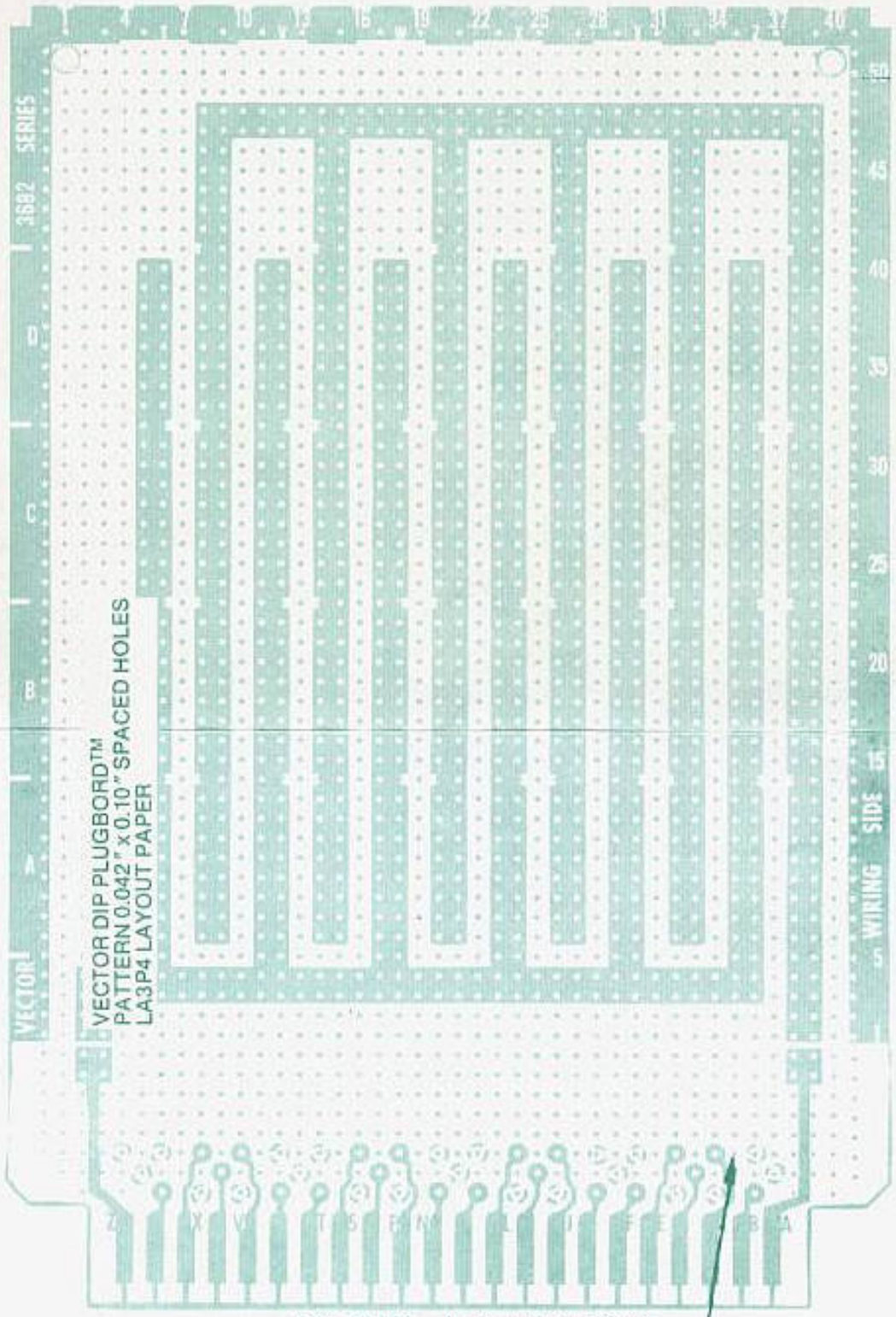
3682-2
6.5" long CARD

LA3P4

WIRING SIDE



CAUTION – In any plug contact area on either side of Plugbord, use only those holes having pads. Holes without pads may have insufficient clearance to adjacent circuitry and using them could cause shorting.

NOTES:

1. Zone letters A, B, C, etc. on Y axis and X, Y, Z on X axis mark position for 14- or 16-pin DIPs.

2. Dotted circles represent plug pads on opposite side of board.

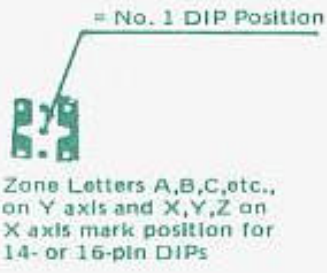
4. To prevent shorting wrapped wire to etched circuit when wire wrapping, use one or more insulated turns at bottom of wrap post; also, do not chisel-cut wire against etched circuit as a shorting burr may occur.

3. Before pressing terminals into board, position (rotate) terminals to maximize the clearance between the widest part of the terminal and the nearest adjacent conductor.

2. Where tin coated circuitry exists, a small percentage of the holes may have solder blockage. This is usually a light "skin" easily penetrated by component leads. In some cases, a soldering iron may be required.

1. Intended for use in non hostile environments up to 200 volts RMS or 300 volts DC.

NOTES:

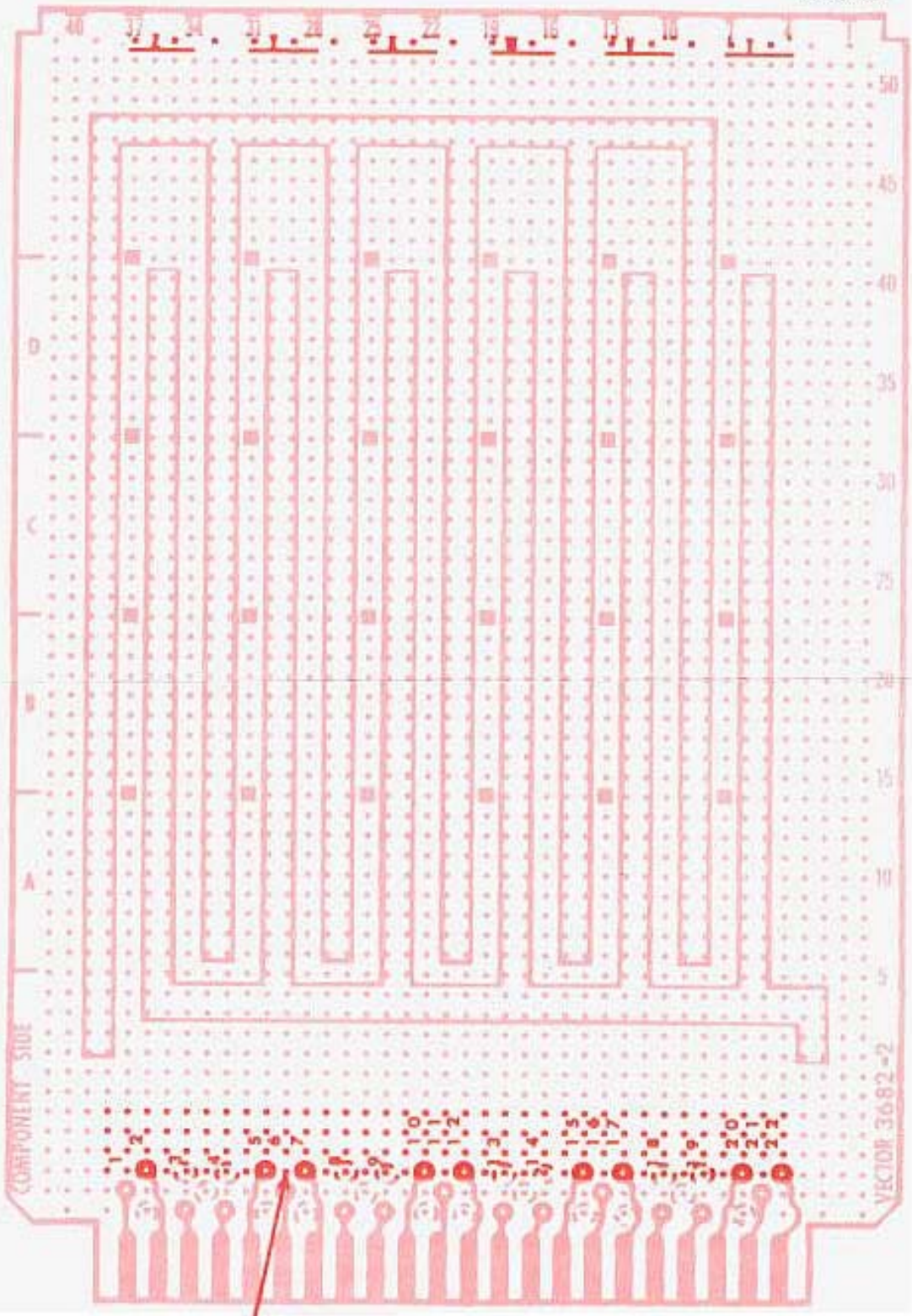


VECTOR ELECTRONICS & TECHNOLOGY, INC.

VECTOR
ELECTRONICS & TECHNOLOGY, INC.

3682-2 6.50" LONG CARD
COMPONENT SIDE

LA3P5



NOTICE: Where tin coated circuitry exists a small percentage of the holes may have solder blockage. This is USUALLY a light "skin" easily penetrated by component leads. In some cases, a soldering iron may be required.

NOTES:

1. BROKEN CIRCLES ABOVE EDGE CONTACTS INDICATE LOCATION OF ACTUAL CONNECTOR CONTACT PADS ON OPPOSITE SIDE OF BOARD.
2. ZONE LETTERS A,B,C, ETC., ON Y AXIS AND X,Y,Z ON X AXIS MARK POSITION FOR 14-OR 16-PIN DIPS.
3. DOTTED CIRCLES REPRESENT PLUG PADS ON OPPOSITE SIDE OF BOARD.
4. INTENDED FOR USE IN NON-HOSTILE ENVIRONMENTS UP TO 200 VOLTS RMS OR 300 VOLTS DC.
5. BEFORE PRESSING TERMINALS INTO BOARD, POSITION (ROTATE) TERMINALS TO MAXIMIZE THE CLEARANCE BETWEEN THE WIDEST PART OF THE TERMINAL AND THE NEAREST ADJACENT CONDUCTOR.
6. TO PREVENT SHORTING WRAPPED WIRE TO ETCHED CIRCUIT WHEN WIRE WRAPPING, USE ONE OR MORE INSULATED TURNS AT BOTTOM OF WRAP POST; ALSO, DO NOT CHISEL-CUT WIRE AGAINST ETCHED CIRCUIT AS A SHORTING BURR MAY OCCUR.

CAUTION:
In any plug contact area on either side of Plugboard, use only those holes having pads. Holes without pads may have insufficient clearance to adjacent circuitry and using them could cause shorting.

= No. 1 DIP Position

VECTOR DIP PLUGBORD™
PATTERN 0.042" x 0.10" SPACED HOLES
LA3-P5 LAYOUT PAPER